•	IN THE UNITED STATES PATENT	AND TRADEMARK OFFICE	7772/8		
Applicant:	Martin C. Roberts et al.	•	2821		
Title:	METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY PROCESS				
Docket No.: Filed: Examiner: Mail Stop An	303.451US6 December 21, 2000 W. David Coleman	Serial No.: 09/745,780 Due Date: July 7, 2004 Group Art Unit: 2823			
Commissioner	73				
P.O. Box 1450					
Alexandria, V	'A 22313-1450				
We are transm	nitting herewith the following attached items	(as indicated with an "X"):			
\overline{X} A Comm X A Termin	postcard. ndment and Response (14 Pages). nunication Concerning Related Applications (nal Disclaimer (3 pgs.). ciate Power of Attorney (1 pg.).	2 pgs.).			
If not provided sufficient numb Account No. 19-	for in a separate paper filed herewith, Please consider of months to enter these papers and please charge-0743.	der this a PETITION FOR EXTENSION Oge any additional fees or credit overpaymen	F TIME for t to Deposit		
SCHWEGMAN, Customer Num	LUNDBERG, WOESSNER & KLUTH, P.A. ber 21186	By. Atty: Charles E. Steffey Reg. No. 25,179			
States Postal Ser	UNDER 37 CFR 1.8: The undersigned hereby certification vice with sufficient postage as first class mail, in an extra 1450, Alexandria, VA 22313-1450, on this day	nvelope addressed to: Mail Stop Amendment,			

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Name

(GENERAL)

Signature

S/N 09/745,780 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Martin C. Roberts et al.

Examiner: W. David Coleman

Serial No.: 09/745,780

00/745 700

Group Art Unit: 2823

Filed:

December 21, 2000

Docket: 303.451US6

Title:

METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT

USING A DUAL POLY PROCESS

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No. 08/912977	Filing Date August 13, 1997	Attorney Docket 303.451US2	Title METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY PROCESS
09/135111 6200892	August 17, 1998	303.451US3	METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY PROCESS
09/134005 5923584	August 14, 1998	303.451US4	DUAL POLY INTEGRATED CIRCUIT INTERCONNECT

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 09/745,780

Filing Date: December 21, 2000

Title: METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY PROCESS

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09/805546 6596632 March 13, 2001

303.451US7

METHOD FOR FORMING AN

INTEGRATED CIRCUIT

INTERCONNECT USING A DUAL

POLY PROCESS

Respectfully submitted,

MARTIN C. ROBERTS ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 373-6970

Date

Rv

Charles E. Steffey

Reg. No. 25,179

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this _____ day of July, 2004.

Name

Signature